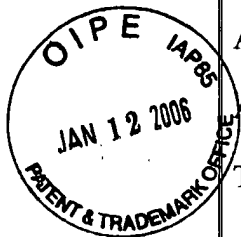


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant(s): Chao-Wu Chen et al.  
Assignee: Mosaic Systems, Inc.  
Title: System and Method for providing a Redundant Memory Array in a Semiconductor Memory Integrated Circuit  
Serial No.: 10/628,896 Filing Date: July 28, 2003  
Examiner: M. Tran Group Art Unit: 2827  
Docket No.: M-15049 US

San Jose, California  
January 9, 2006

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO OFFICE ACTION**

Dear Sir:

This submission responds to the Office Action of June 21, 2005.

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